

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolrich et al. Art Unit :
Serial No. : Examiner :
Filed : February 17, 2004
Title : Memory Mapping In a Multi-Engine Processor

Commissioner for Patents
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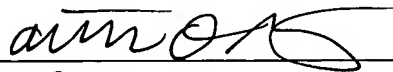
INFORMATION DISCLOSURE STATEMENT

Under 35 USC §120, this application relies on the earlier filing date of application serial number 010736/0367, filed on April 3, 2000. The following references were submitted to and/or cited by the Office in the prior application and, therefore, are not provided in this application:

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 2/17/04



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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-127002	Application No.
	Applicant Wolrich et al.		
	Filing Date	Group Art Unit	

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,717,898	02/1998	Kagan et al.			
	AB	5,142,683	08/1992	Burkhardt, Jr. et al.			
	AC	5,404,464	04/1995	Bennett			
	AD	5,613,071	03/1997	Rankin et al.			
	AE	5,835,755	11/1998	Stellwagen, Jr.			
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	